題號: 394

## 國立臺灣大學 110 學年度碩士班招生考試試題

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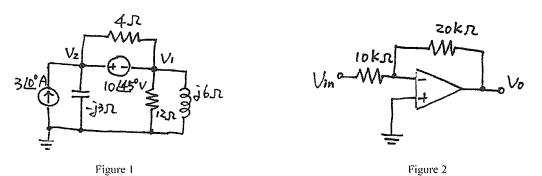
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科目:電路學節次: 7

1. a) Please explain what is the Thevenin's theorem [5] and provide the proof of that. [10]

2. Use superposition to find  $V_1$  in the circuit of Figure 1. [15]

- 3. For the circuit shown in Figure 2 with input voltage  $V_{in}$ =3V, please determine the output voltage  $V_{o}$  under the following conditions: a) the op-amp is ideal [5]; b) the op-amp is not ideal and has the following parameters: input resistance  $R_{i}$ =500k  $\Omega$ , output resistance  $R_{o}$ =200  $\Omega$ , and voltage gain  $A_{v}$ =2x10<sup>4</sup>. [15]
- 4. A  $10V_{rms}$  sinusoidal AC voltage source is supplying the power to a series connected RLC network with R=  $0.2\Omega$ , L=  $20 \mu$  H, and C=  $5 \mu$  F. Please determine the expression of **energy**.  $E_L(t)$ , stored inside the inductor L if the frequency of the voltage source is: a) 60Hz; b) 16kHz. [10]
- 5. For the circuit shown in Figure 3, a 1  $\Omega$  resister is shorted by closing the switch at t=0. Please determine the inductor current  $i_L(t)$  for t>0. [20]
- 6. The s-domain transfer function of a complex network can be expressed as:  $H(s) = \frac{s+10}{s^2+s}$ . Please draw the bode plots (both magnitude and phase) of H(s). Please clearly mark those critical points and the slopes of those curves. [20]



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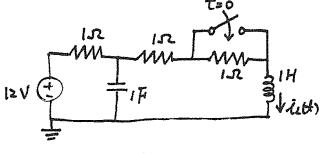


Figure 3

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